

FIG. 1

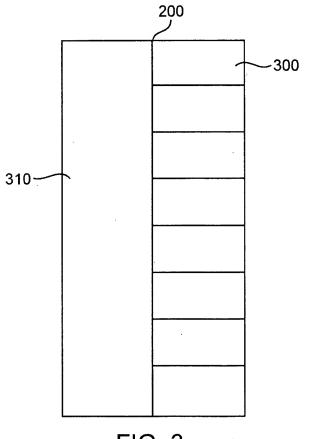


FIG. 3

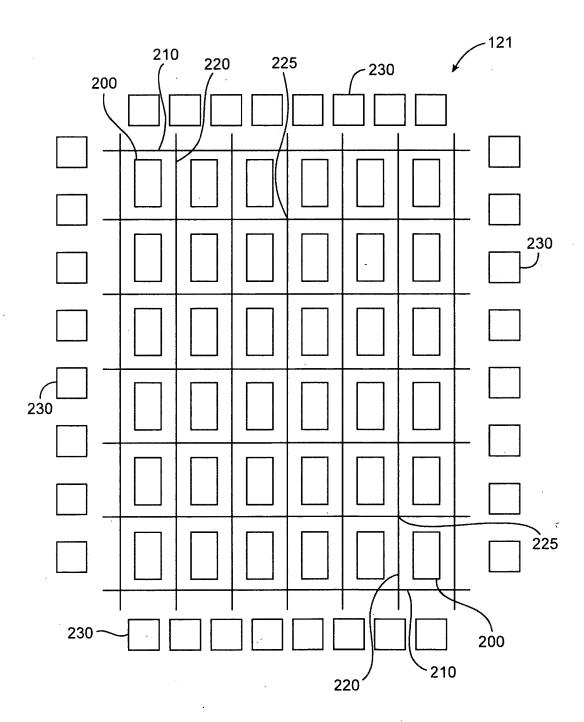


FIG. 2

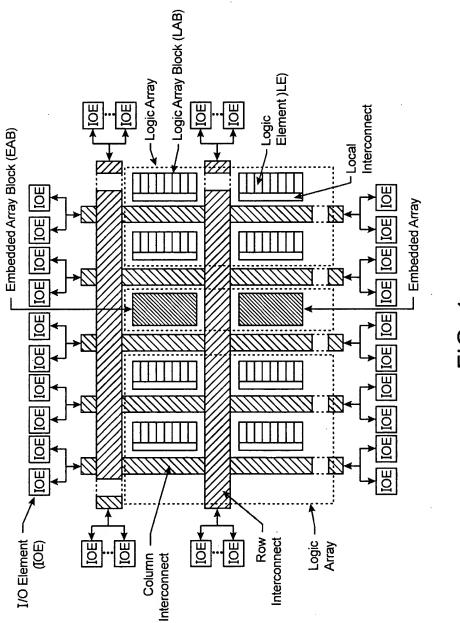
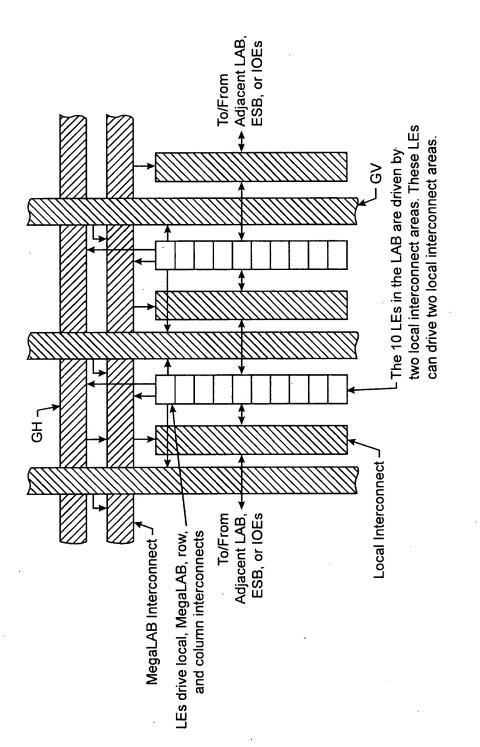


FIG. 4



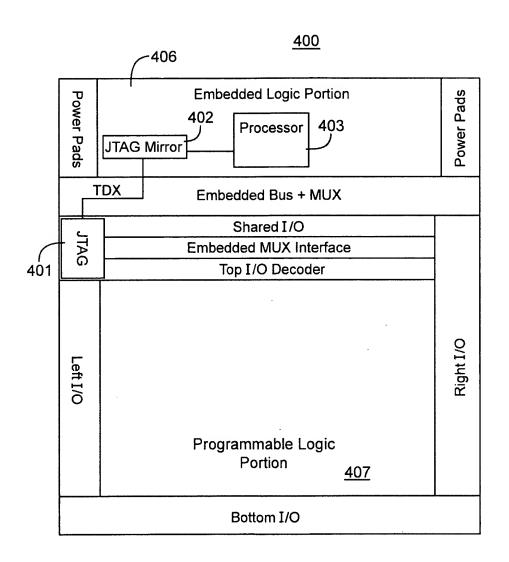


FIG. 6

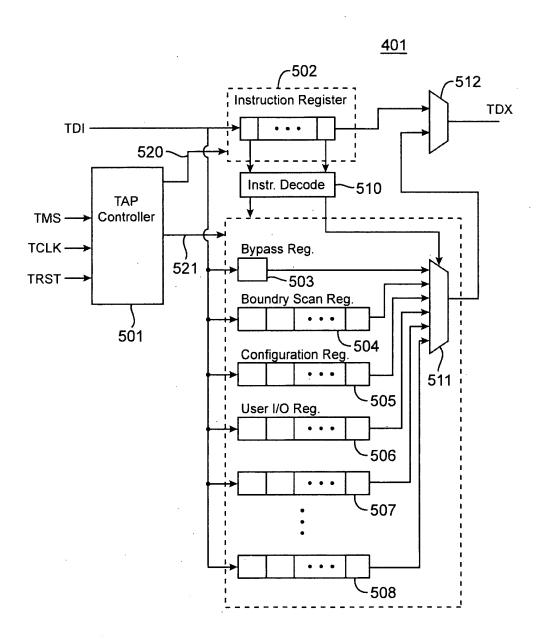


FIG. 7

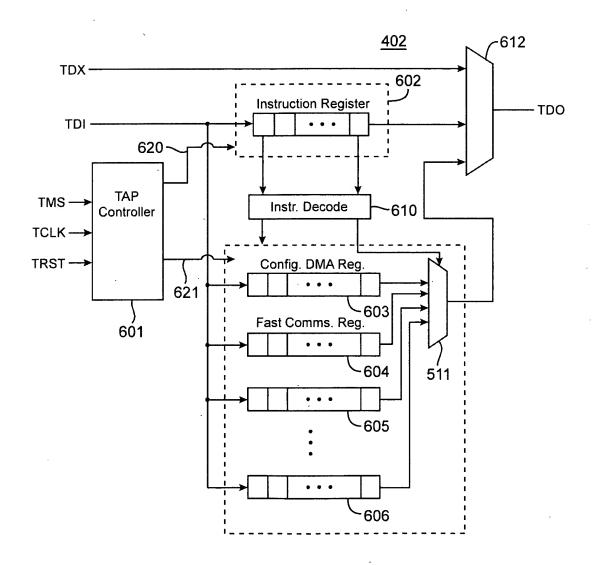


FIG. 8

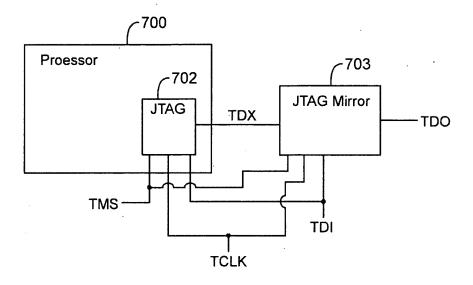


FIG. 9

last bit → 79	78 77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	-	32	31	**	0	→ first bit
wв				0					E		A	CTIC	N			ADDRESS			DATA		

FIG. 10

31 30 29 28 27 26	25 24 23 22 21 20 19 18 1	7 16 15 14 13 1	2 11 10 9 8 7	7 6 5 4	3 2	1	0_					
0												

FIG. 11

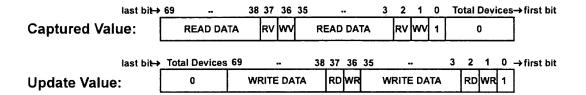


FIG. 12

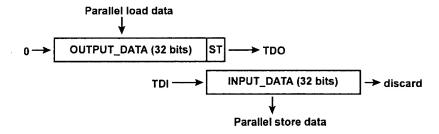


FIG. 13

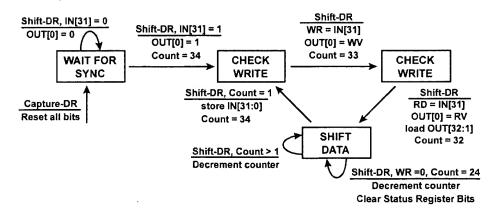


FIG. 14

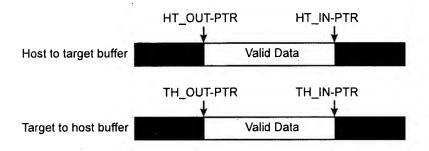


FIG. 15

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
NR	NB		RE		RA	/AIL		١	WSP	ACI	E	WP									1								WE	wв	WF

FIG. 16